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12-20-02  
JL

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/332,271  
Filing Date ..... June 11, 1999  
Inventor ..... Klaus Florian Schuegraf et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2812  
Examiner ..... Ron E. Pompey  
Attorney's Docket No. .... MI22-532  
Title: Methods for Forming Wordlines, Transistor Gates, and Conductive  
Interconnects, and Wordline, Transistor Gate, and Conductive Interconnect  
Structures

**RESPONSE TO AUGUST 13, 2002 FINAL OFFICE ACTION**  
**TO ACCOMPANY CPA FILING**

To: Box CPA  
Assistant Commissioner for Patents  
Washington, D.C. 20231

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**AMENDMENTS**

**In the Claims**

Please replace the claims with the following clean version of the entire set of pending claims, in accordance with 37 C.F.R. §1.121(c)(1)(i). Cancel all previous versions of any pending claim.

A marked up version showing amendments to any claims being changed is provided in one or more accompanying pages separate from this amendment in accordance with 37 C.F.R. §1.121(c)(1)(ii). Any claim not accompanied by a marked up version has not been changed relative to the immediate prior version, except that